



GP
ELECTRONICS

DTA114YCA

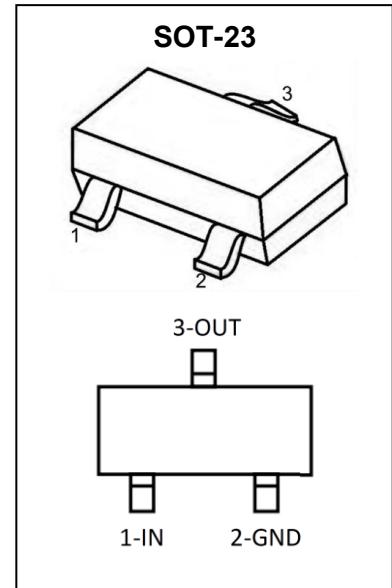
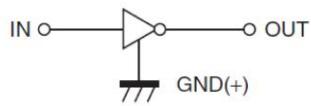
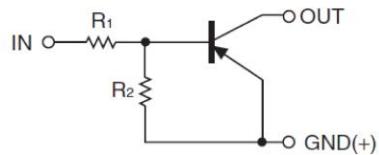
Digital Transistor

DTA114YCA Digital Transistor(PNP)

Feature

- Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors
- The bias resistors consist of thin-film resistors with complete isolation to allow positive biasing of the input .They also have the advantage of almost completely eliminating parasitic effects
- Only the on/off conditions need to be set for operation, making device design easy

Schematic diagram



Marking: H54/54

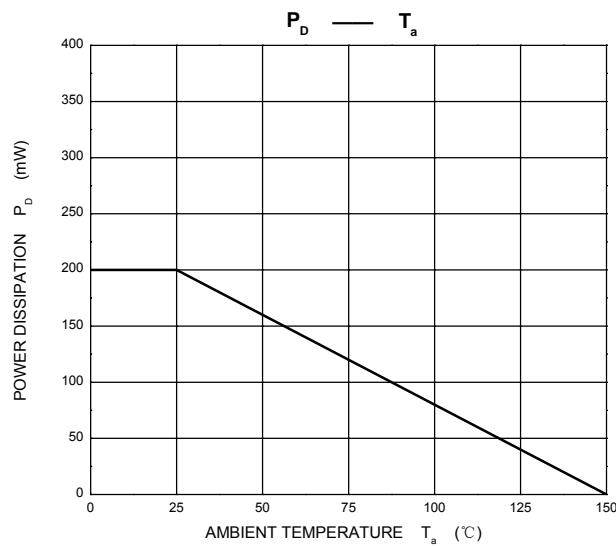
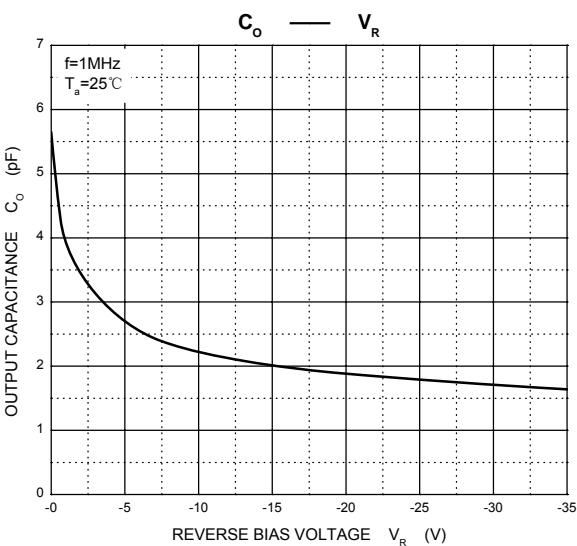
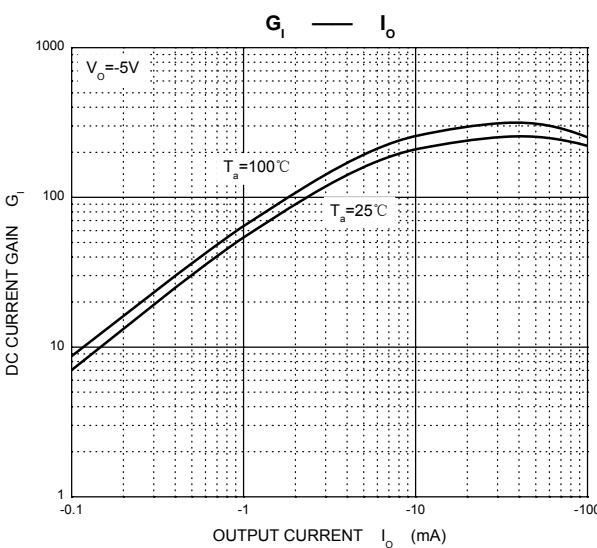
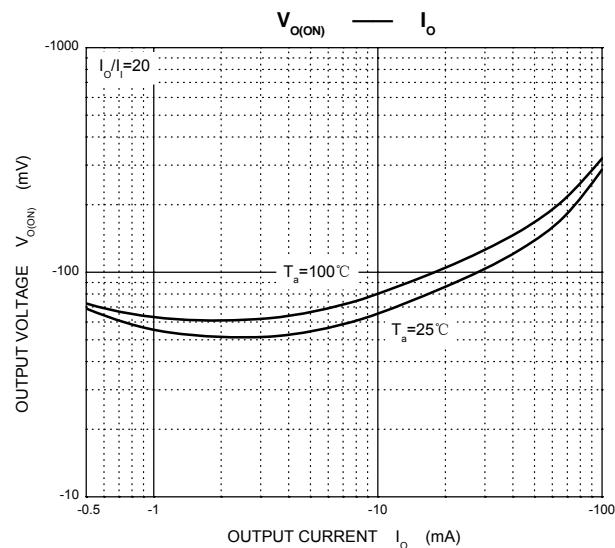
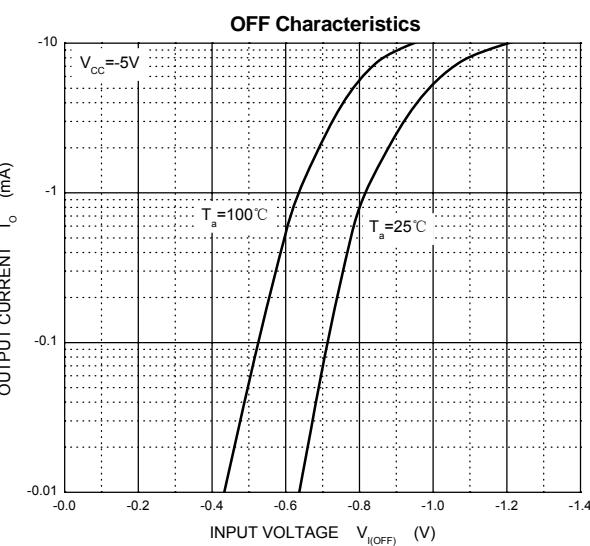
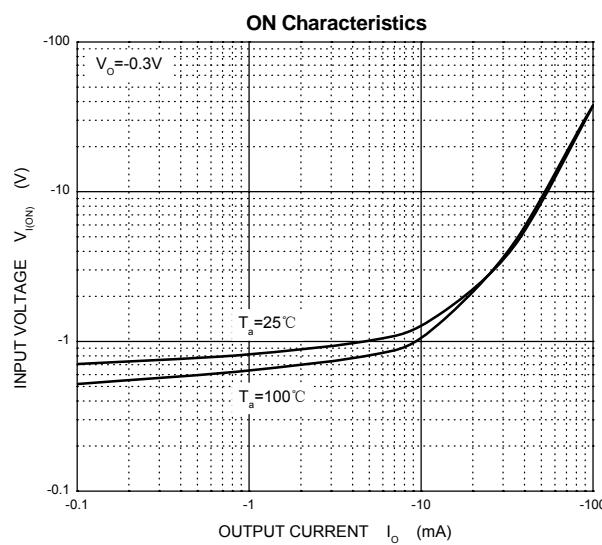
ABSOLUTE MAXIMUM RATINGS (T_A=25°C unless otherwise noted)

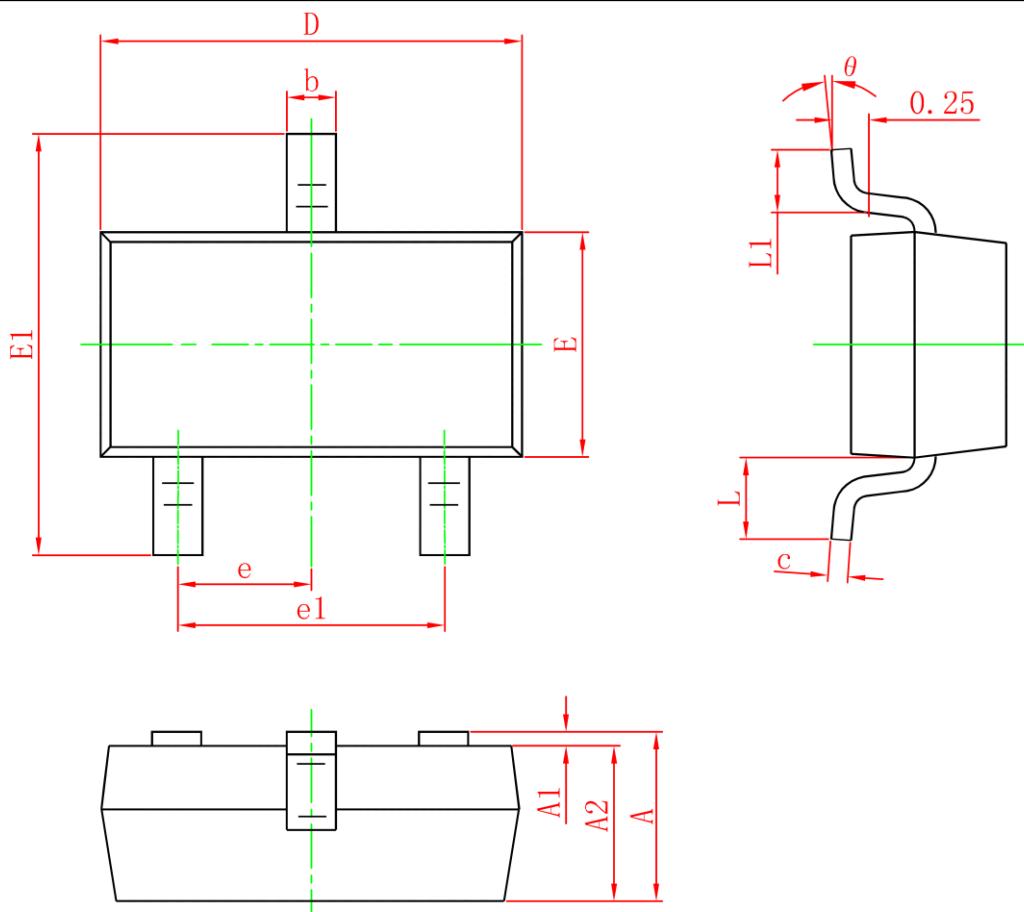
Parameter	Symbol	Value	Unit
Supply Voltage	V _{CC}	-50	V
Input Voltage	V _{IN}	-40~+6	V
Output Current	I _O	-70	mA
Power Dissipation	P _D	200	mW
Junction Temperature	T _J	125	°C
Storage Temperature Range	T _{STG}	-45 ~ +125	°C

ELECTRICAL CHARACTERISTICS (T_A=25°C unless otherwise noted)

Parameter	Symbol	Test Condition	Min	Type	Max	Unit
Input voltage	V _{I(off)}	V _{CC} =-5V , I _O =-100μA	-0.3			V
	V _{I(on)}	V _O =-0.3V , I _O =-1mA			-1.4	V
Output voltage	V _{O(on)}	I _O =-5mA , I _I =-0.25mA		-0.1	-0.3	V
Input current	I _I	V _I =-5V			-0.88	mA
Output current	I _{O(off)}	V _{CC} =-50V , V _I =0V			-0.5	μA
DC current gain	G _I	V _O =-5V , I _O =-5mA	68			
Input resistance	R _I		7	10	13	kΩ
Resistance ratio	R ₂ / R ₁		3.7	4.7	5.7	
Transition frequency	f _T	V _O =-10V,I _O =-5mA,f=100MHz		250		MHz

Typical Characteristics



SOT-23 Package Information


Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	0.900	1.150	0.035	0.045
A1	0	0.100	0	0.004
A2	0.900	1.050	0.035	0.041
b	0.300	0.500	0.012	0.020
c	0.080	0.150	0.003	0.006
D	2.800	3.000	0.110	0.118
E	1.150	1.500	0.045	0.059
E1	2.250	2.650	0.089	0.104
e	0.950TYP		0.037TYP	
e1	1.800	2.000	0.071	0.079
L	0.550REF		0.022REF	
L1	0.300	0.500	0.012	0.020
θ	0°	8°	0°	8°

Attention:

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